

# United States Patent and Trademark Office



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/705,706	11/10/2003	Naoki Fukutomi	566.43481VC5	5226
20457 7	7590 12/29/2005		EXAM	INER
ANTONELLI, TERRY, STOUT & KRAUS, LLP			SCHILLINGER, LAURA M	
1300 NORTH SUITE 1800	SEVENTEENTH STRE	EET	ART UNIT	PAPER NUMBER
	, VA 22209-3873		2813	<u> </u>

DATE MAILED: 12/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary  Examiner Laura M. Schillinger  2813  The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply  A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
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#### **DETAILED ACTION**

### **Priority**

Applicant cannot rely upon the foreign priority papers to overcome this rejection because a translation of said papers has not been made of record in accordance with 37 CFR 1.55. See MPEP § 201.15.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 14-17 are rejected under 35 U.S.C. 102(e) as being anticipated by Hayden et al ('207).

Hayden teaches the following claimed limitations as cited below:

14. A substrate for mounting semiconductor device, comprising:

plural semiconductor-mounting substrate portions (10), each for mounting a respective semiconductor device (12), a connecting portion for connecting said semiconductor-mounting substrate portions (14); and a registration mark portion (100, 102, 104) wherein each of said

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semiconductor-mounting substrate portions (10) comprises wirings that include an external connection terminal (156, 158, 160) and a wire bonding terminal (34/58/14) provided in an outer side than said external connection terminal, and said connecting portion comprises an electrically conductive layer (Col.6, lines:15-21)

- 15. A substrate for mounting a semiconductor device according to claim 14, wherein said electrically conductive layer and said wiring are made of the same material (Col.3, lines: 40-45).
- 16. A substrate for mounting a semiconductor device as claimed in claim 14, wherein nickel and gold are plated on a surface of said wiring (Col.3, lines: 45-50).
- 17. A substrate for mounting a semiconductor device as claimed in claim 15, wherein nickel and gold is plated on a surface of said wiring(Col.3, lines: 45-50).
- 24. A substrate for mounting a semiconductor device as claimed in claim 14, wherein said wire bonding terminal is a terminal for connecting a wire from said semiconductor device thereto (Col.3, lines: 25-45).
- 25. A substrate for mounting a semiconductor device as claimed in claim 24, wherein said external connection terminal is for electrically connecting said substrate to an outer wiring (Col.6, lines: 15-21-mounting substrates and PC boards have connecting wires).

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26. A substrate for mounting a semiconductor device as claimed in claim 14, wherein said external connection terminal is for electrically connecting said substrate to an outer wiring (Col.6, lines: 15-21-mounting substrates and PC boards have connecting wires).

## Response to Arguments

Applicant's arguments with respect to claims 14-17 have been considered but are moot in view of the new ground(s) of rejection.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Laura M. Schillinger whose telephone number is (571) 272-1697. The examiner can normally be reached on M-T, R-F 7:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl W. Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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